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LIST OF PRIOR ART CITED  
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APPLICANT Kriz et al.

FILING DATE November 15, 2001

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## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
AD	AA 4564903	Jan. 14, 1986	Guyette et al.	364/300	Oct. 5, 1983
AD	AB 4843541	Jun. 27, 1989	Bean et al.	364/200	Jul. 29, 1987
AD	AC 5345590	Sep. 6, 1994	Ault et al.	395/650	Sep. 1, 1993

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO

## OTHER PRIOR ART (including author, title, date, pertinent page, etc.)

AD	IBM Technical Disclosure Bulletin, "Hypervisor High Performance Synchronous Dispatch for Coupled Systems", Vol. 39, No. 12, December 1996.
AE	IBM Technical Disclosure Bulletin, "VM MPG Operating as a DRF Hypervisor as a First Level Guest Under PR/SM", Vol. 38, No. 04, April 1995, p. 325.
AF	IBM Technical Disclosure Bulletin, "Sharing Read-Only Memory among Multiple Logical Partitions", Vol. 36, No. 03, March 1993, pp. 303-304.
AG	IBM Technical Disclosure Bulletin, "Highly Parallel Coupling Facility Emulator/Router with Shadowed Link Buffers", Vol. 39, No. 12, December 1986.
AH	IBM Technical Disclosure Bulletin, "Coordinating Multiple Server Partitions to Enter Power-Save State", Vol. 39, No. 06, June 1996, pp. 235-239.

DATE CONSIDERED 10/13/04

EXAMINER

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.